

**Notice of Allowability**

Application No.

10/803,792

Applicant(s)

HER ET AL.

Examiner

Steven D. Radosevich

Art Unit

2117

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 7/9/07.
2. ☒ The allowed claim(s) is/are 1-4, 7-11, 16, 18 and 20.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

### **DETAILED ACTION**

In response to applicant's correspondence to the U.S.P.T.O. on 7/9/2007 the **FINAL** action mailed to the applicant on 5/8/2007 has been **withdrawn**. Claims 1-4, 7-11, and 16-21 are present within this instant examination. Claims 19 and 21 have been canceled by the applicant as indicated on page 5 of 9 of applicant's correspondence to the U.S.P.T.O on 7/9/2007 and will not be given further consideration within this action.

#### ***Priority***

Acknowledgment is made that foreign priority is claimed for this application and as such the data, 3/17/2003, is being used for this examination.

#### ***Drawings***

Acknowledgment is made that the figures are accepted at this time since no further issues are found at this time that would require an objection and/or correction.

#### ***Allowable Subject Matter***

The following is an examiner's statement of reasons for allowance:

Claims 1-4, 7-11, 16, 18, and 20 are allowable.

The present invention pertains to selectively providing output signals in a test mode and normal mode, wherein in the test mode that output signals are provided in a reduced output pin requirement configuration. The claims invention recites features such as: "a plurality of data output pins divided in at least two groups of data output pins; a data processing circuit to generate output signals responsive to an input signal; and an output selection circuit with at least a normal mode and a test mode; where a first group of output signals are provided to a first group of data output pins during a first

test cycle of the test mode; where a second group of output signals are provided to the first group of data output pins during a second test cycle of the test mode; and where during the normal mode: the first group of output signals are provided to the corresponding data output pins of the first group of data output pins; and the second group of output signals are provided to the corresponding data output pins of a second group of data output pins."

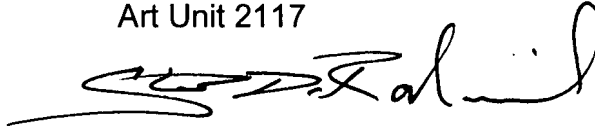
None of the prior art, either taken by itself or in any combination with any other prior art(s), would have anticipated or made obvious the following combination of limitations within the above limitations at or before the time the invention within this application was filed: "a plurality of data output pins divided in at least two groups of data output pins; a data processing circuit to generate output signals responsive to an input signal; and an output selection circuit with at least a normal mode and a test mode; where a first group of output signals are provided to a first group of data output pins during a first test cycle of the test mode; where a second group of output signals are provided to the first group of data output pins during a second test cycle of the test mode; and where during the normal mode: the first group of output signals are provided to the corresponding data output pins of the first group of data output pins; and the second group of output signals are provided to the corresponding data output pins of a second group of data output pins."

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Steven D. Radosevich  
Examiner  
Art Unit 2117

A handwritten signature in black ink, appearing to read 'Steven D. Radosevich', written over the printed name.

/Cynthia Britt/  
Primary Examiner  
Art Unit 2117  
7/17/07